

30V PNP LOW SATURATION MEDIUM POWER TRANSISTOR
Description

This bipolar junction transistor (BJT) is designed to meet the stringent requirement of automotive applications.

Mechanical Data

- Case: SOT89
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 Ⓔ③
- Weight: 0.05 grams (Approximate)

Features

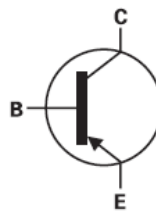
- $BV_{CEO} > -30V$
- $I_C = -5.5A$ Continuous Collector Current
- $I_{CM} = -20A$ Peak Pulse Current
- Low Saturation Voltage $V_{CE(SAT)} < -60mV$ Max @ -1A
- $R_{SAT} = 24m\Omega$ @ -5.5A for Low Equivalent On-Resistance
- Exceptional Gain Linearity Down to -10mA
- h_{FE} Specified up to -20A for High Gain Hold Up
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Applications

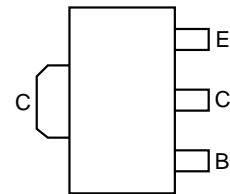
- DC-DC Converters
- MOSFET Gate Drivers
- Charging Circuits
- Power Switches
- Motor Control



Top View



Device Schematic

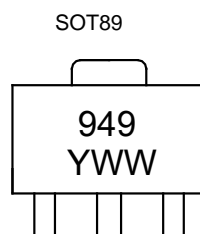


Pin-Out Top View

Ordering Information (Notes 4 and 5)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTP2008ZQTA	949	7	12	1000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to <https://www.diodes.com/quality/>.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information


949 = Product Type Marking Code
 YWW = Date Code Marking
 Y = Last digit of year (ex: 8 = 2018)
 WW = Week code (01 – 53)

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-50	V
Collector-Emitter Voltage	V _{CEO}	-30	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	I _C	-5.5	A
Peak Pulse Current	I _{CM}	-20	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

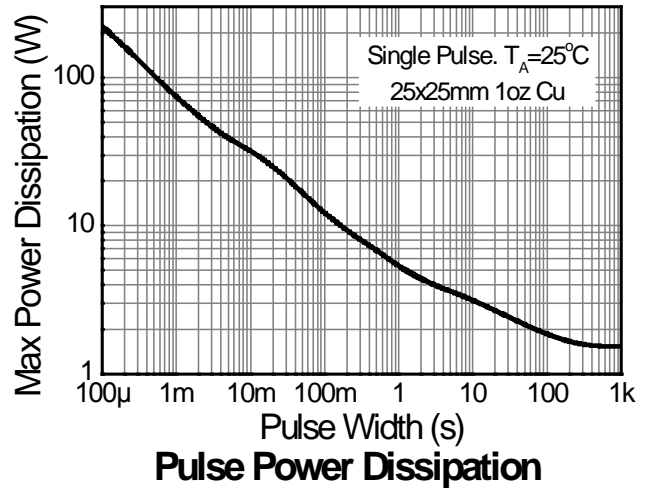
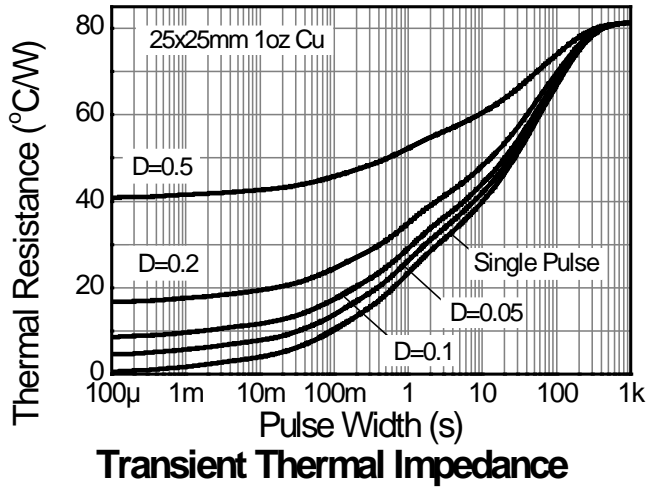
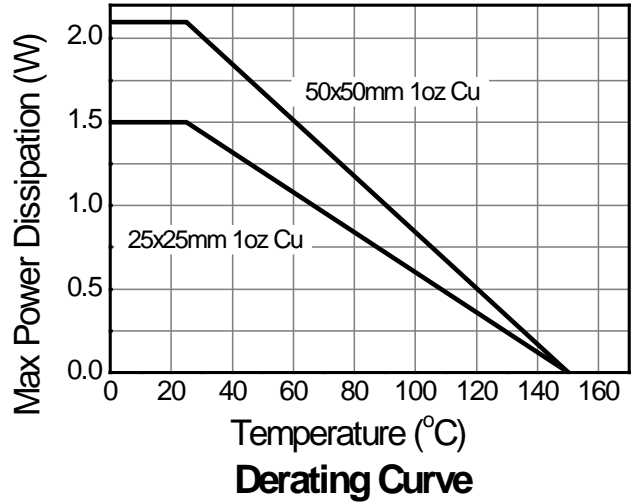
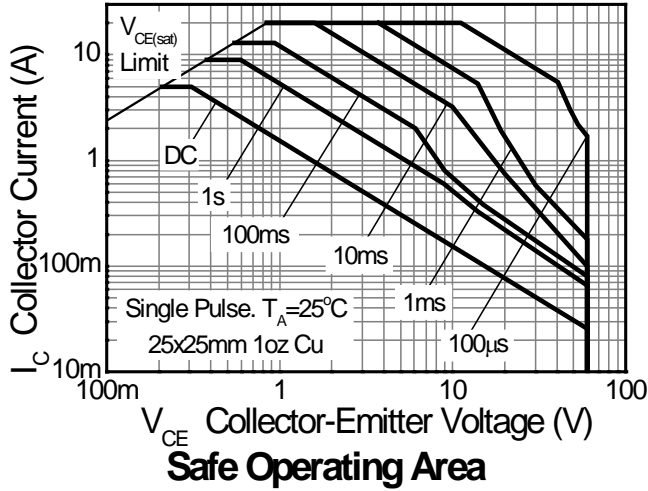
Characteristic	Symbol	Value	Unit
Power Dissipation Linear Derating Factor	P _D	1.5	W
		12	
		2.1	
Thermal Resistance, Junction to Ambient	R _{θJA}	16.8	mW/°C
		83	
Thermal Resistance, Junction to Lead	R _{θJL}	60	°C/W
		3.23	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge—Human Body Model	ESD HBM	4000	V	3A
Electrostatic Discharge—Machine Model	ESD MM	400	V	C

- Notes:
6. For a device mounted with the collector lead on 25mm × 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions while operating in steady-state.
 7. Same as Note 5, except the device is mounted on 50mm × 50mm 1oz copper.
 8. Thermal resistance from junction to solder-point (at the end of the collector lead).
 9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information

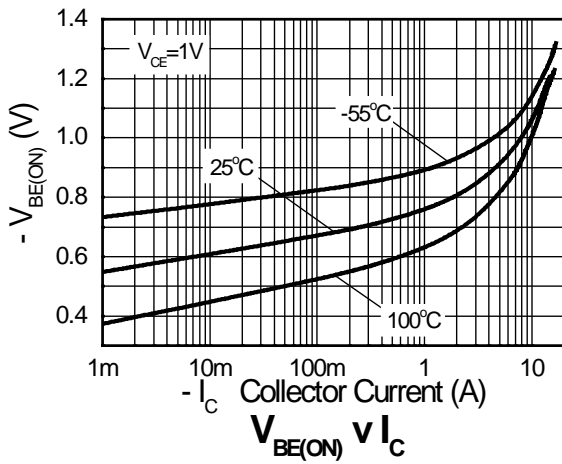
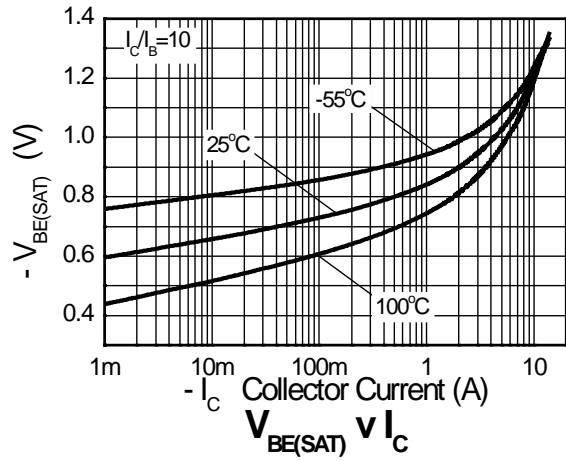
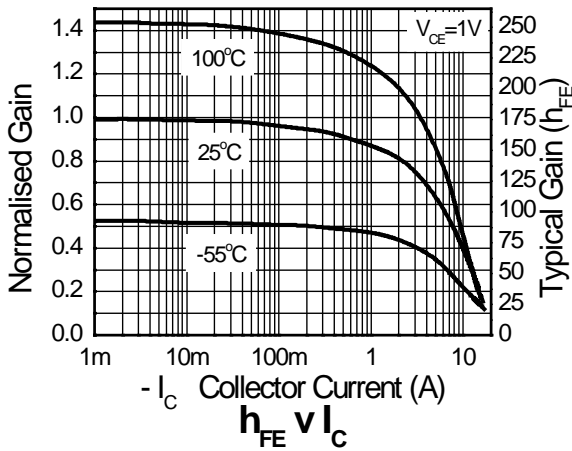
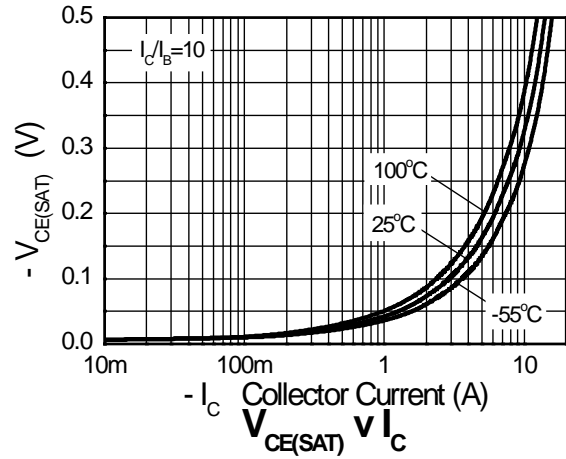
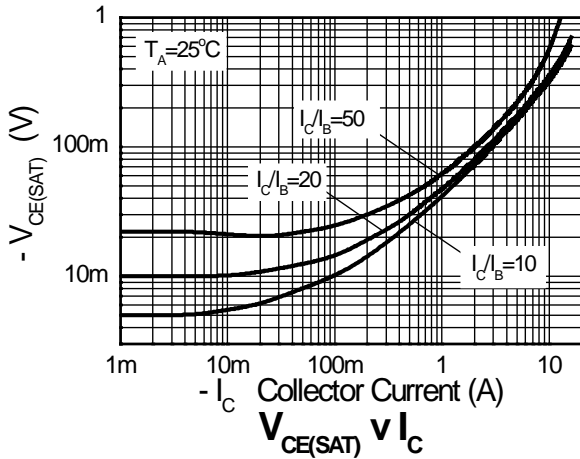


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-50	-70	—	V	I _C = -100μA
Collector-Emitter Breakdown Voltage	BV _{CER}	-50	-70	—	V	I _C = -1μA, R _B ≤ 1kΩ
Collector-Emitter Breakdown Voltage (Note 10)	BV _{CEO}	-30	-40	—	V	I _C = -10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	-8	—	V	I _E = -100μA
Collector Cutoff Current	I _{CBO}	—	< -1	-20	nA	V _{CB} = -40V
		—	—	-0.5	μA	V _{CB} = -40V, T _A = +100°C
Collector Cutoff Current	I _{CER}	—	< -1	-20	nA	V _{CB} = -40V, R ≤ 1kΩ
		—	—	-0.5	μA	V _{CB} = -40V, T _A = +100°C, R ≤ 1kΩ
Emitter Cutoff Current	I _{EBO}	—	< -1	-10	nA	V _{EB} = -6V
Collector-Emitter Saturation Voltage (Note 10)	V _{CE(SAT)}	—	-25 -35 -55 -55 -130	-40 -55 -80 -80 -175	mV	I _C = -0.5A, I _B = -20mA I _C = -1A, I _B = -100mA I _C = -1A, I _B = -20mA I _C = -2A, I _B = -200mA I _C = -5.5A, I _B = -500mA
Base-Emitter Saturation Voltage (Note 10)	V _{BE(SAT)}	—	-970	-1070	mV	I _C = -5.5A, I _B = -500mA
Base-Emitter Turn-On Voltage (Note 10)	V _{BE(ON)}	—	-860	-960	mV	I _C = -5.5A, V _{CE} = -1V
DC Current Gain (Note 10)	h _{FE}	100 100 70 10	225 200 145 20	300	—	I _C = -10mA, V _{CE} = -1V I _C = -1A, V _{CE} = -1V I _C = -5A, V _{CE} = -1V I _C = -20A, V _{CE} = -1V
Transition Frequency	f _T	—	110	—	MHz	V _{CE} = -10V, I _C = -100mA, f = 50MHz
Output Capacitance (Note 10)	C _{OBO}	—	83	—	pF	V _{CB} = -10V, f = 1MHz
Switching Times	t _{ON}	—	43	—	ns	V _{CC} = -10V, I _C = -1A, I _{B1} = -I _{B2} = 100mA
	t _{OFF}	—	230	—		

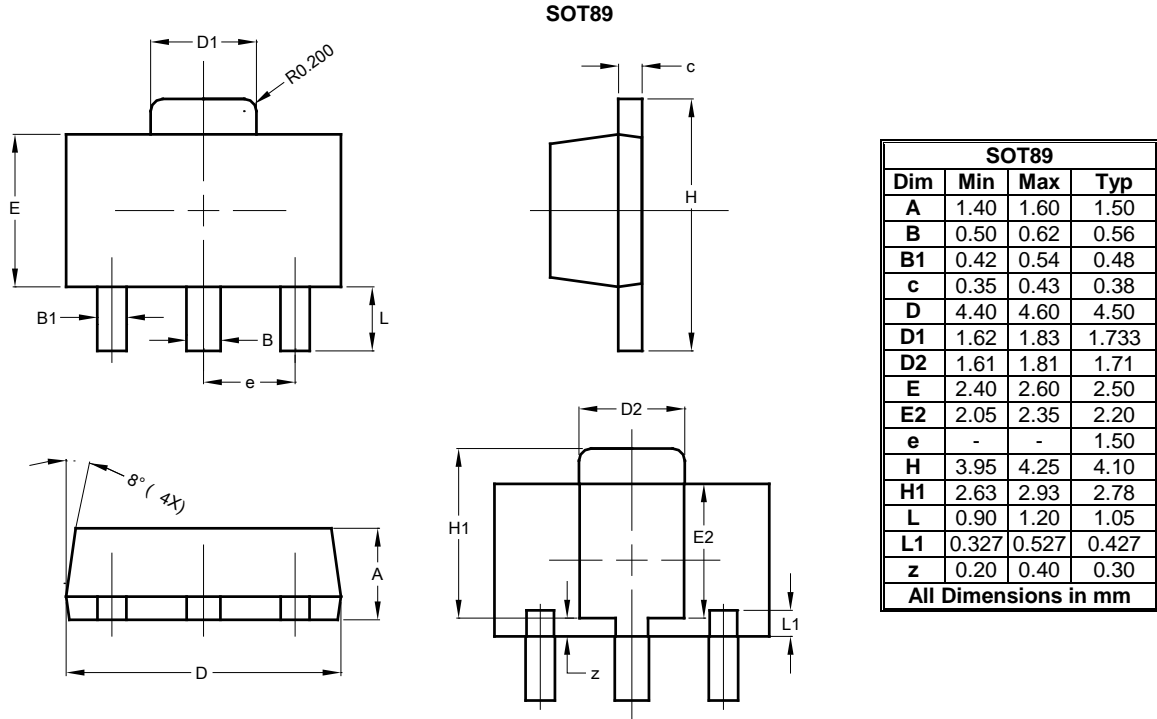
Note: 10. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



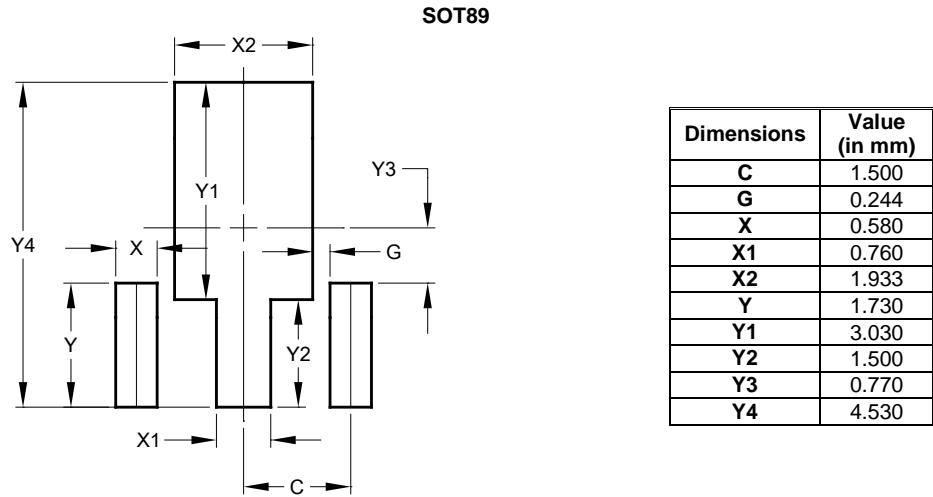
Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Suggested Pad Layout

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