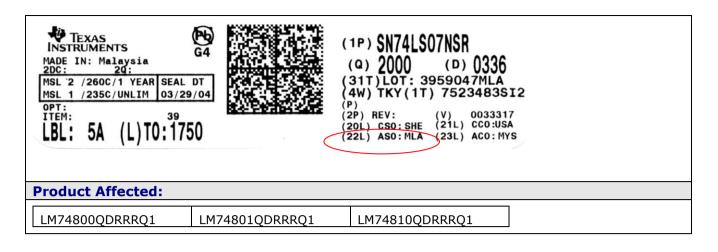
PCN Number	CN Number: 20220712002.2 PCN Date: July 13, 2022										
Title: Oual	Title: Qualification of UTL1 as an additional assembly and test site for select Devices										
Customer Co		PCN Manager		,	lity Services						
					ample requ		A				
Proposed 1 st	Ship Dat	e: Jan 9	, 2023		accepted		Aug 1:	3, 2022*			
*Sample requests received after Aug 13, 2022 will not be supported.											
Change Type	et i										
Assembly	Site		Desig	jn		Wafer Bump Site					
Assembly	Process		Data	Sheet		Wafer Bump Material					
Assembly	Materials		Part ı	number chang	ge 🗌	Wafe	r Bump	Process			
	al Specifica		🔀 Test :	Site			r Fab Si				
Packing/S	Shipping/La	abeling	Test	Process			r Fab Ma				
				Wafer Fab Proces							
	PCN Details										
Description of	Description of Change:										
Texas Instruments Incorporated is announcing the qualification of UTL1 as an alternate Assembly site and test site for devices listed below in the product affected section. Construction differences are as follows:											
]				CDAT	L U	TL1					
·	<u>N 10</u>			-			-				
	Mount Co	mpound		4207123	SID#	PZ003	Z0035				
Test coverage test MQ		s, conditior	ns will rem	ain consister	it with curre	nt test	ing and	verified with			
Reason for C											
Supply continu	uity										
Anticipated i	mpact on	Form, Fit	, Functio	n, Quality o	r Reliabilit	y (pos	itive /	negative):			
None											
Impact on Er	nvironme	ntal Ratin	gs								
	Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.										
RoH	S	R	EACH	Gre	en Status		IEC	62474			
🛛 No Chang	e	🛛 No Ch	ange	🛛 No C	Change		No Cha	ange			
				· ·	-						
Changes to p	Changes to product identification resulting from this PCN:										
Assembly Site	e Assen	nbly Site Ori	gin (22L)	Assembly Cou	ntry Code (23	SL)	Assembly City				
CDAT		CDA		C	HN		Chengdu				
UTL1		NSE		THA			Bangkok				
	Sample product shipping label (not actual product label)										





TI Information Selective Disclosure

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Qualification of second assembly source for LM74810QDRRQ1 and metal spins LM74801QDRRQ1, LM74800QDRRQ1 (Q100H, Grade1, -40/125C) Approve Date 07-Apr-2022

Product Attributes

Attributes	Qual Device: LM74800QDRRQ1	Qual Device: LM74801QDRRQ1	Qual Device: LM74810QDRRQ1	QBS Product Reference: LM74800QDRRRQ1 <u>A1</u>	QBS Product Reference: LM74801QDRRRQ1 <u>A1</u>	QBS Product Reference: LM74810QDRRRQ1 <u>A1</u>	QBS Product Reference: LM74810QDRRRQ1 <u>A0</u>	QBS Process Reference: LM74700QDBV- <u>B0</u>
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range	-40 to +125 C	-40 to +125 C	-40 to +125 C	-40 to +125 C	-40 to +125 C			
Product Function	Power Management	Power Management	Power Management	Power Management	Power Management	Power Management	Power Management	Power Management
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB
Die Revision	A1	A1	A1	A1	A1	A1	A0	B0
Assembly Site	UTAC	UTAC	UTAC	CDAT	CDAT	CDAT	CDAT	NS2 (UTAC2)
Package Type	WSON	WSON	WSON	WSON	WSON	WSON	WSON	SOT23
Package Designator	DRR	DRR	DRR	DRR	DRR	DRR	DRR	DBV
Ball/Lead Count	12	12	12	12	12	12	12	6

- QBS: Qual By Similarity

- Qual Devices LM74800QDRRRQ1, LM74801QDRRRQ1, and LM74810QDRRRQ1 are qualified at LEVEL2-260C

Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

Typ e	#	Test Spec	Mi n Lo t Qt y	SS/L ot	Test Name / Condition onment Stress	Durati on	Qual Device: <u>LM74800QD</u> <u>RRQ1</u>	Qual Device: <u>LM74801QD</u> <u>RRQ1</u>	Qual Device: <u>LM74810QD</u> <u>RRQ1</u>	QBS Product Reference: LM74800QDR <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74801QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74810QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: LM74810QDR <u>RRQ1 A0</u>	QBS Process Reference : LM74700Q DBV-B0
Test	Group	JEDE	elerate	ea Envir	onment Stress	s lests								
PC	A 1	C J- STD- 020 JESD 22- A113	3	77	Automotive Preconditio ning	Level 2- 260C	-	-	Pass	-	-	-	Pass	Pass
HA ST	A 2	JEDE C JESD 22- A110	3	77	Biased HAST, 130C/85% RH	96 Hours	-	-	3/231/0	-	-	-	3/231/0	3/231/0
AC	A 3	JEDE C JESD 22- A102	3	77	Autoclave 121C	96 Hours	-	-	3/231/0	-	-	-	3/231/0	3/231/0
тс	A 4	JEDE C JESD 22- A104 and Appen dix 3	3	77	Temperatur e Cycle, - 65/150C	500 Cycles	-	-	3/231/0	-	-	-	3/231/0	2/154/0
TC- BP	A 4	MIL- STD8 83 Metho d 2011	1	60	Post TC Bond Pull	500 Cycles	-	-	3/60/0	-	-	-	3/60/0	1/5/0
РТС	A 5	JEDE C JESD 22- A105	1	45	Power Temperatur e Cycle	1000 Cycles	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A

Typ e	#	Test Spec	Mi Lo t Qt y	SS/L ot	Test Name / Condition	Durati on	Qual Device: <u>LM74800QD</u> <u>RRQ1</u>	Qual Device: LM74801QD RRQ1	Qual Device: <u>LM74810QD</u> <u>RRQ1</u>	QBS Product Reference: <u>LM74800QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74801QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74810QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74810QDR</u> <u>RRQ1 A0</u>	QBS Process Reference : LM74700Q DBV-B0
HTS L	A 6	JEDE C JESD 22- A103	1	45	High Temp Storage Bake 150C	1000 Hours	-	-	3/135/0	-	-	-	-	-
HTS L	A 6	JEDE C JESD 22- A103	1	45	High Temp Storage Bake 175C	500 Hours	-	-	-	-	-	-	3/135/0	1/45/0
Test G	roup		elerate	ed Lifeti	me Simulation	Tests								
HT OL	B 1	JEDE C JESD 22- A108	3	77	Life Test, 150C	408 Hours	-	-	-	-	-	-	-	2/154/0
HT OL	B 1	JEDE C JESD 22- A108	3	77	Life Test, 125C	1000 Hours	-	-	-	-	-	-	3/231/0	-
ELF R	В 2	AEC Q100- 008	3	800	Early Life Failure Rate, 150C	24 Hours	-	-	-	-	-	-	-	3/2400/0
ED R	В 3	AEC Q100- 005	3	77	NVM Endurance, Data Retention, and Operational Life	-	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A
Test	Grou	<u> пр С – Ра</u>	ckage	e Assem	bly Integrity T									
WB S	C 1	AEC Q100- 001	1	30	Auto Wire Bond Shear	Minimu m of 5 device s, 30 wires Cpk>1. 67	-	-	3/90/0	-	-	-	3/90/0	-

	yp e	#	Test Spec	Mi Lo t Qt y	SS/L ot	Test Name / Condition	Durati on	Qual Device: <u>LM74800QD</u> <u>RRQ1</u>	Qual Device: <u>LM74801QD</u> <u>RRQ1</u>	Qual Device: <u>LM74810QD</u> <u>RRQ1</u>	QBS Product Reference: LM74800QDR RRQ1 A1	QBS Product Reference: LM74801QDR RRQ1 A1	QBS Product Reference: LM74810QDR RRQ1 A1	QBS Product Reference: LM74810QDR RRQ1 A0	QBS Process Reference : LM74700Q DBV-B0
	VB P	C 2	MIL- STD8 83 Metho d 2011	1	30	Auto Wire Bond Pull	Minimu m of 5 device s, 30 wires Cpk>1. 67	-	-	3/90/0	-	-	-	3/90/0	-
s	SD	C 3	JEDE C JESD 22- B102	1	15	Auto Solderabilit y (Pb and Pb-Free)	>95% Lead Covera ge 8 Hr Steam Age	-	-	1/15/0	-	-	-	1/20/0	-
F	P	C 4	JEDE C JESD 22- B100 and B108	3	10	Auto Physical Dimensions	Cpk>1. 67	-	-	3/30/0	-	-	-	3/30	-
	Test	t Gro	oup D – D	ie Fal	bricatio	n Reliability Te	ests								
E	M	D 1	JESD 61	-	-	Electromigr ation	-	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	-	-	-	-	-
	D)B	D 2	JESD 35	-	-	Time Dependant Dielectric Breakdown	-	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	-	-	-	-	-
н	ICI	D 3	JESD 60 & 28	-	-	Hot Injection Carrier	-	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	-	-	-	-	-
	BT I	D 4	-	-	-	Negative Bias Temperatur e Instability	-	Completed Per Process Technology	Completed Per Process Technology	Completed Per Process Technology	-	-	-	-	-
Ty e		#	Test Spec	Mi Lo t Qt y	SS/L ot	Test Name / Condition	Durati on	Qual Device: <u>LM74800QD</u> <u>RRQ1</u>	Qual Device: <u>LM74801QD</u> <u>RRQ1</u>	Qual Device: <u>LM74810QD</u> <u>RRQ1</u>	QBS Product Reference: <u>LM74800QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74801QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: <u>LM74810QDR</u> <u>RRQ1 A1</u>	QBS Product Reference: LM74810QDR RRQ1 A0	QBS Process Reference : <u>LM74700Q</u> <u>DBV-B0</u>
								Requiremen ts	Requiremen ts	Requiremen ts					
s	м	D 5	-	-	-	Stress Migration	-	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	Completed Per Process Technology Requiremen ts	-	-	-	-	-
				Elect	rical Ve	rification Test	S								
H	1	E 2	AEC Q100- 002 AEC	1	3	ESD - HBM - Q100	2500 V	-	-	-	1/3/0	1/3/0	1/3/0	-	-
C N		E 3	Q100- 011	1	3	ESD - CDM - Q100 ESD - CDM	500 V	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	-	-
C N		E 3	AEC Q100- 011	1	3	- Q100 (Corner Pins)	1000 V	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	-	-
L	U	E 4	AEC Q100- 004	1	6	Latch-up	Latchu p- 2/125C Cpk>1.	-	-	-	1/6/0	1/6/0	1/6/0	-	-
E	D	E 5	AEC Q100- 009	3	30	Auto Electrical Distribution s	67 Room, hot, and cold test	1/30/0	1/30/0	1/30/0	1/30/0	1/30/0	1/30/0	3/90/0	-

A1 (PC): Preconditioning: Performed for THB, Biased HAST, AC, uHAST, TC & PTC samples, as applicable.

Ambient Operating Temperature by Automotive Grade Level: Grade 0 (or E): -40°C to +150°C Grade 1 (or Q): -40°C to +125°C Grade 2 (or T): -40°C to +105°C Grade 3 (or 1): -40°C to +85°C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level): Room/Hot/Cold: HTOL, ED Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU Room: AC/uHAST

Green/Pb-free Status: Qualified Pb-Free(SMT) and Green

TI Qualification ID: 20200421-133774

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

Location	E-Mail						
WW Change Management Team	PCN ww admin team@list.ti.com						

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