

Description

The FMD-4206S is a fast recovery diode of 600 V / 20 A. The maximum $t_{\rm rr}$ of 50 ns is realized by optimizing a life-time control.

Features

•	V_{RM}	600 V
•	$I_{F(AV)}$	- 20 A
	V _F	
•	t _{rr1}	50 ns

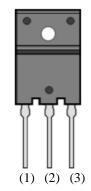
Bare Leads: Pb-free (RoHS Compliant)
Flammability: Equivalent to UL94V-0

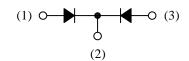
Applications

- PFC Circuit
- Inverter Circuit

Package

TO3PF-3L





- (1) Anode
- (2) Cathode
- (3) Anode

Not to scale

FMD-4206S

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Rating	Unit
Nonrepetitive Peak Reverse Voltage ⁽¹⁾	V_{RSM}		600	V
Repetitive Peak Reverse Voltage ⁽¹⁾	V_{RM}		600	V
Average Forward Current	$I_{F(AV)}$	See Figure 1 and Figure 2	20	A
Surge Forward Current ⁽¹⁾	I_{FSM}	Half cycle sine wave, positive side, 10 ms, 1 shot	100	A
I ² t Limiting Value ⁽¹⁾	I^2t	$1 \text{ ms} \le t \le 10 \text{ ms}$	50	A^2s
Junction Temperature	$T_{\rm J}$		-40 to 150	°C
Storage Temperature	T_{STG}		-40 to 150	°C

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Formand Walters Dury(1)	V_{F}	$T_J = 25 ^{\circ}\text{C}, I_F = 10 \text{A}$	_	_	1.7	V
Forward Voltage Drop ⁽¹⁾		$T_J = 100 ^{\circ}\text{C}, I_F = 10 \text{A}$	_	1.1	_	V
Reverse Leakage Current ⁽¹⁾	I_R	$V_R = V_{RM}$	_	_	100	μA
Reverse Leakage Current under High Temperature ⁽¹⁾	$H \cdot I_R$	$V_R = V_{RM}, T_J = 150 ^{\circ}C$	_		300	μΑ
Payansa Pagayany Tima(1)	t _{rr1}	$I_F = I_{RP} = 500 \text{ mA},$ 90% recovery point, $T_J = 25 \text{ °C}$	_		50	ns
Reverse Recovery Time ⁽¹⁾	t _{rr2}	$I_F = 500$ mA, $I_{RP} = 1$ A, 75% recovery point, $T_J = 25$ °C	_	_	30	ns
Thermal Resistance (2)	R _{th(J-C)}		_	_	2.0	°C/W

Mechanical Characteristics

Parameter	Conditions	Min.	Тур.	Max.	Unit
Heatsink Mounting Screw Torque		0.686	_	0.882	N·m
Package Weight		_	6.5	_	g

⁽¹⁾ Specifies a value per chip; the FMD-4206S consists of two chips.

⁽²⁾ Refers to thermal resistance between junction and the case. The case temperature is measured at the backside near the screw hole.

Rating and Characteristic Curves

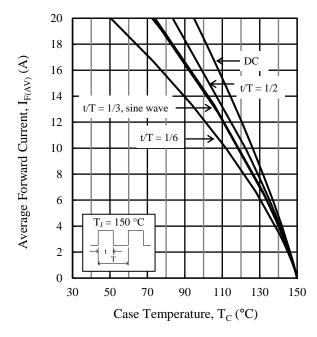


Figure 1. Typical Characteristics: $I_{F(AV)}$ vs. T_{C} $(V_{R}=0\ V)$

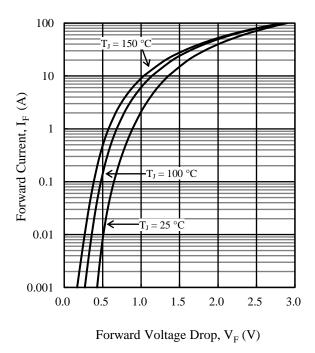


Figure 3. Typical Characteristics: I_F vs. V_F

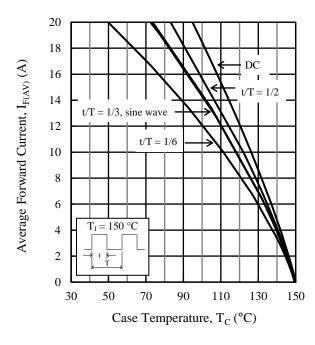


Figure 2. Typical Characteristics: $I_{F(AV)}$ vs. T_{C} ($V_{R}=600\ V$)

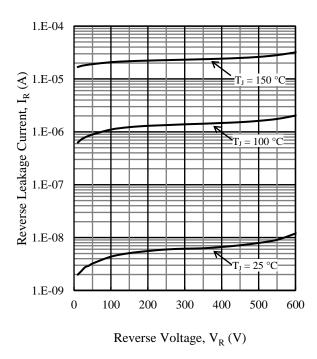
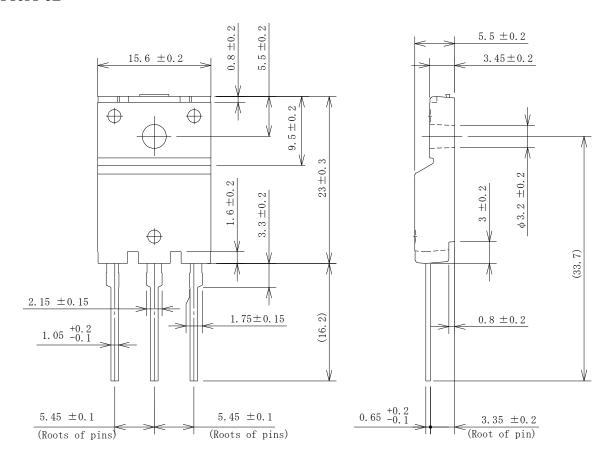
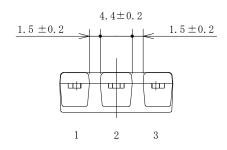


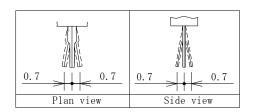
Figure 4. Typical Characteristics: I_R vs. V_R

Physical Dimensions

• TO3PF-3L







NOTES:

- Dimensions in millimeters
- Maximum gate burr height is 0.3 mm.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time within the following limits:

Flow: 260 °C / 10 s, 1 time

Soldering Iron: $350 \, ^{\circ}\text{C} \, / \, 3.5 \, \text{s}, \, 1 \, \text{time}$

Soldering should be at a distance of at least 1.5 mm from the body of the product.

Marking Diagram

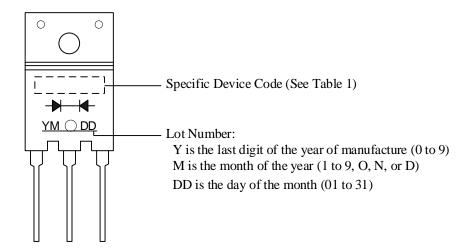


Table 1. Specific Device Code

Specific Device Code	Part Number
D4206S	FMD-4206S

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