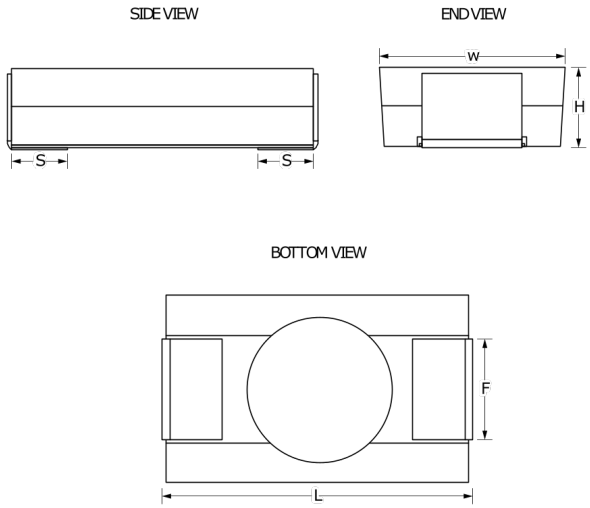
A720V477M2R5APE003

A720, Polymer Aluminum, 470 uF, 20%, 2.5 VDC, -55/+105°C, Height Max = 2.1mm



Click here for the 3D model.

General Information	
Series	A720
Dielectric	Polymer Aluminum
Style	SMD Chip
Description	Surface Mount, Polymer Aluminum
RoHS	Yes
Termination	Nickel Palladium Gold
Qualifications	UL94V-0
AEC-Q200	No
Halogen Free	Yes
Component Weight	141 mg
Notes	V/7343-21.
Footprint	7343
Shelf Life	104 Weeks
MSL	3

Dimensions	
L	7.3mm +/-0.3mm
W	4.3mm +/-0.3mm
H	1.9mm +/-0.2mm
S	1.3mm +/-0.3mm
F	2.4mm +/-0.1mm

Specifications	
Capacitance	470 uF
Capacitance Tolerance	20%
Voltage DC	2.5 VDC
Temperature Range	-55/+105°C
Rated Temperature	105°C
Dissipation Factor	6%
Resistance	3 mOhms (100kHz 25C)
Ripple Current	11690 mA (rms 100kHz)
Leakage Current	70.5 uA (5min 25°C)

Packaging Specifications	
Packaging	T&R, 180mm
Packaging Quantity	1000

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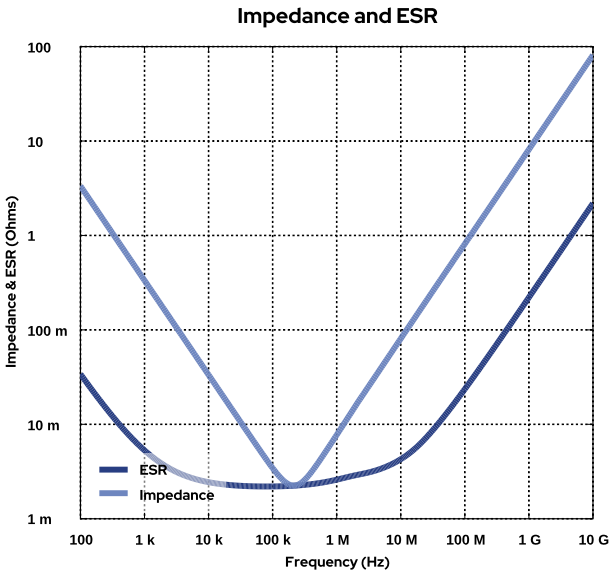
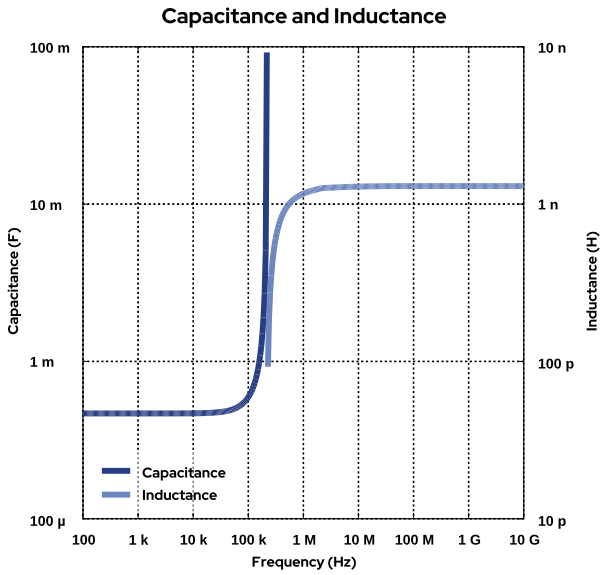
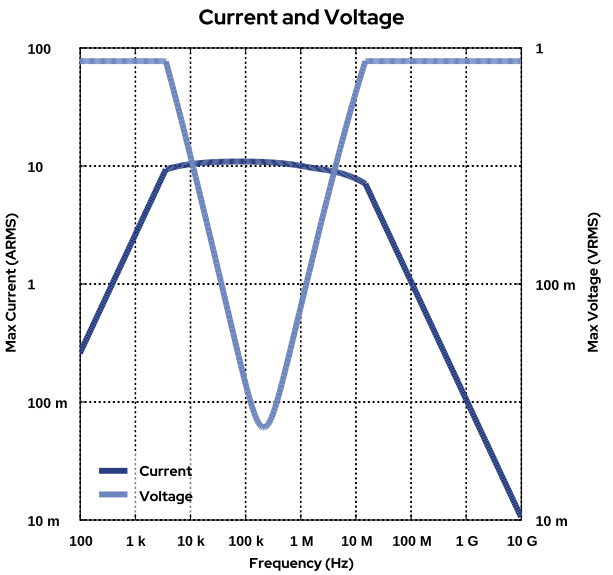
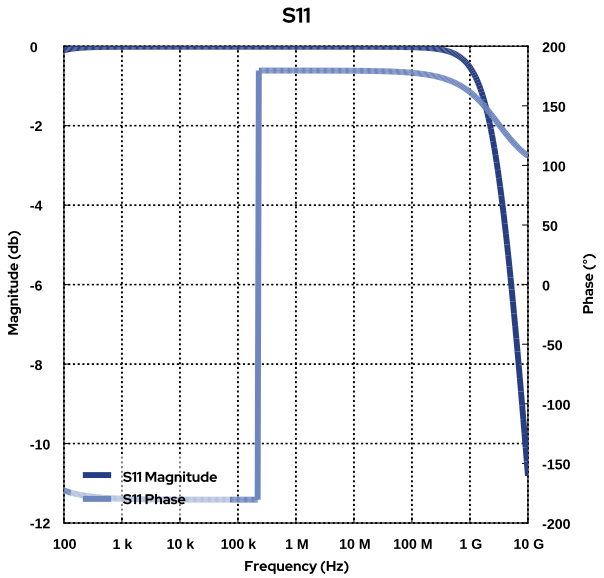
Simulations

For the complete simulation environment please visit K-SIM.









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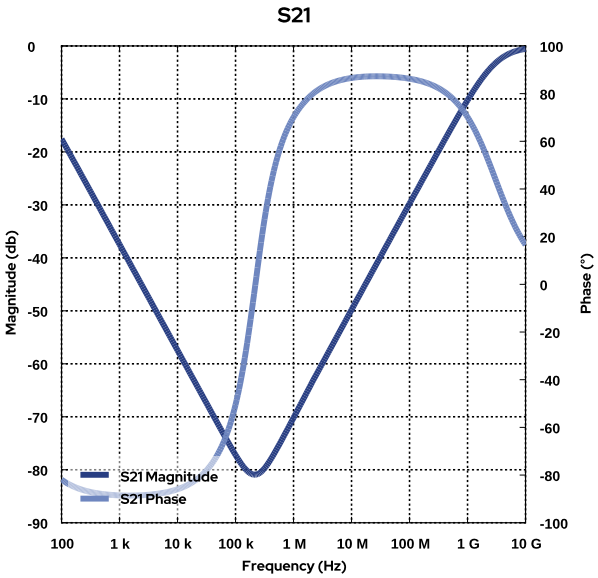

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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.