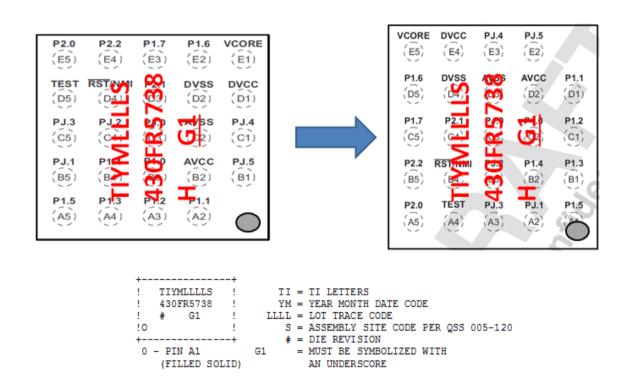
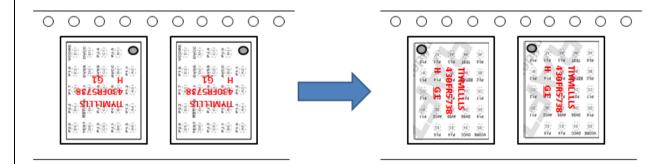
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	sembly Site	-		Design			Waf	er Bur	np Sit	e	
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	sembly Materi			Part number change	е				np Pro	ocess	
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Beyond the DSI		eet changes,	the ir	mportant change to	note is	the	e chan	ge in	the dr	awing o	f
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	VCORE P1.6	P1.7 P2.2 P2. E3 E4 E5]		PJ.5	PJ.4 (E3)	DVCC E4	VCORE (E5)		
	DVCC DVSS	P2.1 RST/NMI TES	ST .		P1.1	AVCC	AVSS	DVSS	P1.6		
	D1 D2 PJ.4 AVSS	D3 (D4) (D5) PJ.0 PJ.2 PJ.			(D1) P1.2	D2) P1.0	(D3) PJ.0	D4) P2.1	D5) P1.7		
	C1 C2	(C3) (C4) (C5			(C1)	(C2)	(C3)	(C4)	(C5)		
	PJ.5 AVCC	P1.0 P1.4 PJ. B3 B4 B5		,	P1.3	P1.4 B2	PJ.2	RST/NMI	P2.2 B5		
	P1.1	P1.2 P1.3 P1. (A3) (A4) (A5)			P1.5	PJ.1	PJ.3	TEST (A4)	P2.0 (A5)		
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						PJ.5	PJ.4	DVCC	VSORE		
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	® 80 00 00 00 00 00 00 00 00 00 00 00 00	SSS SSS C	P1.6		P1.1	D2	D3	DVSS D4	D5		
	A P B P.		P.7		P1.2	P1.0 C2	PJ.0	P2.1	P1.7 (C5)		
	F(B) F(S)	PJ.2	P2.2		P1.3	P1.4 B2	PJ.2	RST/NMI	P2.2 B5		
		=	.		→P1.5		PJ.3	TEST	P\$.0		
	A 15		P2.0		(A1)	A2		(A4)	(A5)		

Top View of Package: Symbo Change



Tape and Reel needs to change: Device rotated 180DEG in Pocket



NOTE: Requirement: Pin1 (A1) Marker needs to be at punch row side

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



MSP430FR5739, MSP430FR5738, MSP430FR5737, MSP430FR5736, MSP430FR5735 MSP430FR5734, MSP430FR5733, MSP430FR5732, MSP430FR5731, MSP430FR5730

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Changes from June 15, 2014 to April 25, 2016

Page

•	Added Section 3.1, Related Products	6
•	Changed all ball coordinates on YQD package (rotated package relative to prior orientation)	. 9
•	Changed all ball coordinates for the YQD package in Table 4-1, Signal Descriptions	10
•	Moved T _{stg} to Absolute Maximum Ratings, and removed Handling Ratings table	15
•	Added Section 5.2, ESD Ratings	15
•	Added Section 5.6. Thermal Resistance Characteristics	18
•	Changed the note that starts "Tools that access the Spy-Bi-Wire and BSL interfaces"	38
•	Changed all cases of "bootstrap loader" to "bootloader"	48
•	Corrected spelling of NMIIFG in Table 6-6, System Module Interrupt Vector Registers	51
•	Changed ball coordinates for the YQD package in Table 6-8, TAO Signal Connections	
•	Changed ball coordinates for the YQD package in Table 6-9, TA1 Signal Connections	
•	Changed ball coordinates for the YQD package in Table 6-10, TBO Signal Connections	
•	Moved and changed the title of Section 6.11, Input/Output Diagrams	
•	Switched P1SEL0.x and P1SEL1.x in P1.0 to P1.2 schematic to show correct inputs to multiplexers	69
•	Switched P1SEL0.x and P1SEL1.x in P1.3 to P1.5 schematic to show correct inputs to multiplexers	71
•	Switched P1SEL0.x and P1SEL1.x in P1.6 and P1.7 schematic to show correct inputs to multiplexers	73
•	Switched P2SEL0.x and P2SEL1.x in P2.0 to P2.2 schematic to show correct inputs to multiplexers	74
•	Switched P2SEL0.x and P2SEL1.x in P2.3 and P2.4 schematic to show correct inputs to multiplexers	75
•	Switched P2SEL0.x and P2SEL1.x in P2.5 and P2.6 schematic to show correct inputs to multiplexers	77
•	Switched P2SEL0.x and P2SEL1.x in P2.7 schematic to show correct inputs to multiplexers	78
•	Switched P3SEL0.x and P3SEL1.x in P3.0 to P3.3 schematic to show correct inputs to multiplexers	79
•	Switched P3SEL0.x and P3SEL1.x in P3.4 to P3.6 schematic to show correct inputs to multiplexers	81
•	Switched P3SEL0.x and P3SEL1.x in P3.7 schematic to show correct inputs to multiplexers	
•	Switched P4SEL0.x and P4SEL1.x in P4.0 schematic to show correct inputs to multiplexers	
•	Switched P4SEL0.x and P4SEL1.x in P4.1 schematic to show correct inputs to multiplexers	
•	Switched PJSEL0.x and PJSEL1.x in PJ.0 to PJ.2 schematic to show correct inputs to multiplexers	
•	Switched PJSEL0.x and PJSEL1.x in PJ.3 schematic to show correct inputs to multiplexers	85
•	Switched PJSEL0.4 and PJSEL1.4 in PJ.4 schematic to show correct inputs to multiplexers	87
•	Switched PJSEL0.5 and PJSEL1.5 in PJ.5 schematic to show correct inputs to multiplexers	
•	Moved Section 6.12, Device Descriptors (TLV)	
•	Replaced former section Development Tools Support with Section 7.3, Tools and Software	94
•	Updated Section 7.4, Documentation Support	96

The datasheet number will be changing.

Device Family	Change From:	Change To:
MSP430FR5738	SLAS639J	SLAS639K

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/MSP430FR5738

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
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